AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning on page 10, line 5, with the following amended paragraph:

External circuit 9 is configured of pulse width measurement detection circuit 5.

Please replace the paragraph beginning on page 10, line 6 and ending on line 7, with the following amended paragraph:

Internal circuit 18 includes clock divider circuit 1, delay circuit 85, EXOR circuit 4, pulse width measurement circuit 5, and latch circuit 6.

Please replace the paragraph beginning on page 10, line 13 and ending on line 18, with the following amended paragraph:

When pulse width <u>measurement</u> detection circuit 5 receives from EXOR circuit 4 through terminal 101 a signal having a pulse of no less than a predetermined width, it outputs data of the high level through terminal 102 to latch circuit 6. When pulse width <u>measurement</u> detection circuit 5 receives from EXOR circuit 4 a signal having a pulse of less than the predetermined width, it outputs data of the low level through terminal 102 to latch circuit 6.